



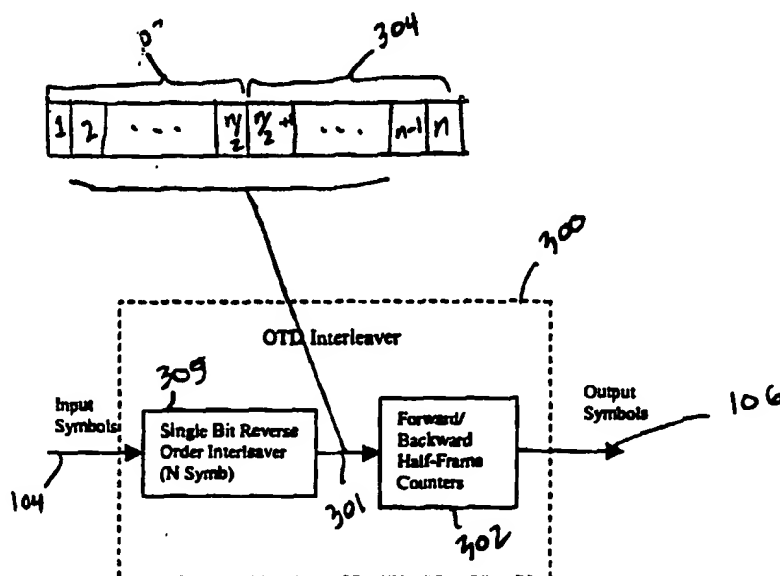
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(71) Applicant: MOTOROLA INC. [US/US]; 1303 East Algonquin Road, Schaumburg, IL 60196 (US).			
(72) Inventors: CHEN, Jiangnan; 602 70th Street, Darien, IL 60561 (US). JALLOUL, Louay; Apartment #104, 1501 Churchill Drive, Palatine, IL 60067 (US).			
(74) Agents: BELADI, Sayed, Hossein et al.; Motorola Inc., Intellectual Property Dept., 1303 East Algonquin Road, Schaumburg, IL 60196 (US).			

(54) Title: INTERLEAVING METHOD AND APPARATUS FOR ORTHOGONAL TRANSMIT DIVERSITY AND MULTI-CARRIERS CDMA COMMUNICATION SYSTEMS

(57) Abstract

A communication system provides a method and apparatus for interleaving data symbols of a block of data symbols (301) having data symbols from B(1) to B(n). The method and the accompanying apparatus include dividing block of data symbols (301) into a plurality of groups of data symbols (303 and 304). Each of the plurality of groups of data symbols (303, 304) includes data symbols equal in number to a fraction of data symbols B(1) through B(n), such as 1/2 and 1/3, such that the total number of data symbols in plurality of groups of data symbols (303, 304) substantially equals "n" data symbols. An interleaved block of data symbols (106) is formed by an alternating selection of data symbols from plurality groups of data symbols (303, 304). The alternating selection from each of plurality of groups of data symbols (303, 304) is according to either a forward addressing or a backward addressing mapping. A plurality of coded data symbols in a block of coded data symbols (104) is received for interleaving according to a single bit reversal interleaving function to form block of data symbols (301).



Interleaving Scheme

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CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
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Interleaving Method and Apparatus for Orthogonal Transmit Diversity and Multi-Carriers CDMA Communication Systems

Background of the Invention

5 The invention relates to the field of communications and communication systems, more particularly, to a code division multiple access (CDMA) communication system.

Orthogonal transmit diversity (OTD) is a feature of a CDMA communication system implemented for improving performance of a communication link
10 between a base station and a mobile station. The commonly known IS-2000 standard provides a detailed description for implementation and use of an OTD feature in a CDMA communication system. A copy of the IS-2000 standard may be obtained by contacting Telecommunications Industry Association, 2500 Wilson Blvd., Suite 300, Arlington, VA 22201
15 USA, or visiting a world web site on the internet located at <http://www.tiaonline.org/>, which incorporated by reference herein. The OTD feature is applicable for both single carrier CDMA system and multi-carriers (MC) CDMA system.

Generally speaking, information data in a CDMA system is coded according
20 to a coding rate. For example, coding rates of $1/3$, $1/2$ and $1/4$ are commonly used in CDMA systems operating according to IS-2000 standards; other

coding rates are also available. For example, when the information data is coded according to a $1/3$ coding rate, the encoder produces 3 data symbols for every input information data bit, similarly for other encoding rates. One ordinary skilled in the art may appreciate that data symbol and data bit are interchangeable terms. After the information data has been encoded, the encoded symbols are transmitted to a receiving unit for decoding and recovery of the information data bits. The transmission of data normally takes place over a channel with varying propagation characteristics. As a result, if the encoded symbols are transmitted sequentially from a transmitting source such that all or most of the encoded symbols experience the same or similar propagation characteristics in the transmission channel, the information data may not be decoded or recovered at the receiver due to severe degradation in the received signal energy of all or most of the encoded symbols.

The OTD provides a method for transmission of encoded symbols such that, as much as possible, each encoded data symbol of an information data bit experiences a propagation characteristics independent of other encoded symbols of the same information data bit. As such, the possibility of degrading all or most of the encoded data symbols of the same information data bit due to a varying propagation channel characteristics is spread and minimized. A system implementing the OTD feature transmits

encoded data symbols of an information data bit from at least two transmit antennas, such that one encoded symbol is transmitted from one antenna and another encoded symbol from another antenna. The encoded symbols transmitted from each antenna are spread using an orthogonal Walsh code
5 different than the Walsh code used for the encoded symbols transmitted from the other antenna. A mobile station, or a remote receiving unit, receiving both signals obtains a reception diversity improvement by allowing the decoder to process a group of symbols that have faded independently. In order to ensure that coded symbols from the same
10 information data bit fade independently in transmission, use of two parallel interleavers in the transmitter may be required according to the description provided in the earlier versions of IS-2000 standard and related literatures. Referring to FIG. 1, a CDMA communication system transmitter block diagram 100 is shown. Raw information data bits are added with CRC bits
15 and some other tail data bits in a block 101 to produce information data bits 102. Information data bits 102 are fed to a channel encoder 103 for encoding according to an encoding rate to produce encoded symbols 104. In case of implementing an OTD feature, the encoded symbols 104 are fed to an OTD interleaver 105; otherwise, the encoded symbols 104 are fed to a
20 single interleaver before further processing of the transmit signal. After interleaving, interleaved symbols 106 are produced. Interleaved symbols

106 pass through a transmit signal processing block 107 which may include long code masking, power control operation and power control puncturing operations; such operations are well known in the art and more specifically explained in the IS-2000 standard. Block 107 produces data
5 symbols 108 which pass through a data splitter 109 to produce two streams of data symbols 110 and 111. The data symbols 110 and 111 are modulated independently in QPSK modulators 112 and 113 and up-converted to an appropriate carrier frequency before transmission from antennas 114 and 115. On a receiving end both signals transmitted from
10 antennas 114 and 115 are demodulated, decoded and combined to recover the information data bits.

The OTD interleaver 105 requires at least two data block interleavers 120 and 121. The information data bits 102 are normally transmitted in a block of data format, as such, the encoded symbols are also in a block of encoded
15 symbols format. Normally, a block of information data bits is set to occupy a predefined time frame. The number of information data bits in a time frame varies depending on the information data bit rate. Time frames of 5, 10, 20, 40 and 80 mSec. are possible. The information data rate also may vary from 9.6 kbps up to 460.8 kbps. Calculating the number of
20 information data bits or encoded symbols in a time frame is well known by one ordinary skilled in the art. Similarly, the number of encoded symbols

depends on the encoding rate and the number of information data bits in a time frame. For example, one block of encoded symbols 104 in a 20 mSec time frame may include 384 encoded symbols for an information data bit rate 9.6 Kbps in a 20 mSec time frame using an encoding rate of $\frac{1}{2}$.

- 5 A block of encoded symbols 104 are de-muxed in a de-mux block 122 to produce two halves, blocks of symbols 123 and 124 which have the same time frame and equal to the time frame of the block of encoded symbols 104. The number of symbols in each block of symbols 123 and 124 is equal to one half of the encoded symbols in the block of encoded symbols 104.
- 10 Two data block interleavers 120 and 121 according to a prior art provide the interleaving functions for interleaving each block of symbols 123 and 124. If the number of encoded symbols in the block of encoded symbols 104 is equal to "N", the number of symbols in each block 123 and 124 is equal to "N/2". As a result, the interleavers 120 and 121 perform
- 15 interleaving functions for "N/2" symbols. The operation of interleaving is according to a known and predetermined algorithm. Such algorithm often may be governed by a mathematical relationship mapping the input to-output symbols. The interleavers 120 and 121 then output interleaved symbols 125 and 126 of block of encoded symbols 123 and 124. The OTD
- 20 interleaver 105 may additionally for improving performance may include a circular shift operation 127 shifting data symbols by a predetermined

number. The circular shift function 127 shifts the data symbols of, in this case, the interleaved symbols 126 to produce data symbols 128. Data symbols 128 and 125 are muxed in a mux-ing block 129 to produce interleaved symbols 106.

5 Referring to FIG. 2, a case of multi-carrier (MC) CDMA communication system, the OTD MC interleaver 201 may be used. Interleaver 201 inputs encoded symbols 104 and outputs interleaved symbols 106 according to its internal OTD operations. A block of input encoded symbols is de-muxed into three blocks 202, 203 and 204 in a demux-ing block 205 for multi-
10 carrier CDMA system with three CDMA carriers. Each block of symbols 202, 203 and 204 pass through an interleaver block respectively shown as interleaver blocks 206, 207 and 208 to produce interleaved blocks of symbols 209, 210 and 211 respectively. Interleaved symbols 210 and 208 pass through circular shift operations 212 and 213 to produce symbols
15 214 and 215. Symbols 209, 214 and 215 are muxed in a mux-ing operation 216 to produce interleaved symbols 106. In case of OTD-MC interleaver 201, the data splitter 109 splits the data block into three blocks of data for transmission from up to three transmit antennas. The modulation and frequency up-conversion of transmission may be
20 performed independently for three carrier frequencies. Each carrier may be

transmitted from a respective antenna, or two carries from one antenna and the third one from a second antenna.

As shown in FIGs 1 and 2, the prior art requires at least two block interleavers, and in case of MC-OTD, at least three block interleavers. The

5 operations within such block interleavers are intensive and are often governed by one or more mathematical relationships to map the data symbols of an input block of data symbols to data symbols of an output block of data symbols, in addition to other operations such as muxing, demuxing and circular shifts operations. As such, operations relating to
10 multiple block interleavers take extensive hardware and software resources.

Therefore, there is a need for a method and apparatus for providing OTD feature in a CDMA communication system without a requirement of multiple block interleavers.

15

Brief Description of the Drawings

FIG. 1 depicts a CDMA communication system transmitter block diagram having an OTD interleaver block according to prior art.

FIG. 2 depicts a case of multi-carrier (MC) CDMA communication system
20 having an OTD MC interleaver according to prior art.

FIG. 3 depicts an OTD interleaver block diagram according to various aspects of the invention.

FIG. 4 depicts an MC-OTD interleaver according to various aspects of the invention for a case of multi-carriers (MC) CDMA communication system.

Table 1 shows an interleaver parameters for various data rates.

FIG. 5 depicts for $m=7$ and $N=3$ a matrix of data symbols where each
5 number shown represents an input address, and the location in the matrix is with respect to other data symbols at an output. The arrows indicate the order of the each entry in the matrix.

Detailed Description of the Preferred Embodiment(s)

10 Broadly speaking, various aspects of the invention in a communication system include a method and apparatus for interleaving data symbols of a block of data symbols having data symbols from $B(1)$ to $B(n)$. The method and the accompanying apparatus include dividing the block of data symbols into a plurality of groups of data symbols. Each of the plurality of
15 groups of data symbols includes data symbols equal in number to a fraction of data symbols $B(1)$ through $B(n)$, such that the total number of data symbols in the plurality of groups of data symbols substantially equals "n" data bits. Furthermore, an interleaved block of data symbols is formed by an alternating selection of data symbols from the plurality
20 groups of data symbols. The alternating selection from each of the plurality of groups of data symbols is according to either a forward addressing or a

backward addressing mapping. According to various aspects of the invention, a plurality of coded data symbols in a block of coded data symbols is received for interleaving according to a single bit reversal interleaving function to form the block of data symbols.

5 More specifically, referring to FIG. 3, an OTD interleaver 300 block diagram according to various aspects of the invention is shown. The OTD interleaver 300 may be a direct substitute for the prior art OTD interleaver 105 shown in Fig. 1. In a communication system, such as a CDMA communication system, a method and apparatus for interleaving data symbols of a block of
10 data symbols 301 that includes data symbols from $B(1)$ to $B(n)$, according to various aspects of the invention, includes dividing block of data symbols 301 into a first and second groups of data symbols 303 and 304. First group of data symbols 303 includes data symbols from $B(1)$ to $B(n/2)$, and second group of data symbols 304 includes data symbols from $B(n/2 + 1)$
15 to $B(n)$. An interleaved block of data symbols 106 is formed by an alternating selection of data symbols from first and second groups of data symbols 303 and 304 by a symbol counter 302. The alternating selection of data symbols by symbol counter 302 begins in first group of data symbols 303 beginning from the $B(1)$ data symbol and alternating to second group
20 of data symbols 304 beginning from the $B(n)$ data symbol. Furthermore, the alternating selection ends with the data symbol $B(n/2)$ in first group of

data symbols 303 and the data symbol $B(n/2 + 1)$ in second group of data symbols 304.

The alternating selection of data symbols from first group of data symbols 303 is according to a forward addressing beginning from the $B(1)$ to $B(n/2)$ data symbols, such that after selection of the $B(1)$ data symbol, a data symbol $B(2)$ is selected the next time the selection is from first group of data symbols 303. The alternating selection of data symbols from second group of data symbols 304 is according to a backward addressing beginning from the $B(n)$ to $B(n/2 + 1)$ data symbols, such that after selection of the data symbol $B(n)$, a data symbol $B(n-1)$ is selected the next time the selection is from second group of data symbols 304.

Various aspects of the invention further includes receiving a plurality of coded data symbols 104 in a block of coded data symbols, and interleaving by an interleaver 309 block of coded data symbols 104 according to a single bit reversal interleaving function to form block of data symbols 301.

The bit reversal interleaver 309 input symbols are written sequentially at addresses, N_{in} from 0 to block length minus 1, then the symbols are read out of the interleaver from addresses $N_{out} = 2^m (N_{in} \bmod N) + \text{Bit_Rev}_m(\lfloor N_{in}/N \rfloor)$, where $\lfloor x \rfloor$ denotes the largest integer less than or equal to x , and $\text{Bit_Rev}_m(y)$ denotes the bit-reversed m -bit value of y .

Referring to FIG. 4, an MC-OTD interleaver 400 for a case of multi-carriers (MC) CDMA communication system is shown. The MC-OTD interleaver 400 may be a direct substitute for the prior art MC-OTD interleaver 201 shown in Fig. 2. According to various aspects of the invention, interleaving data symbols of a block of data symbols 401 having data symbols from $B(1)$ to $B(n)$ includes dividing block of data symbols 401 into a first, second and third groups of data symbols 402, 403 and 404 respectively. First group of data symbols 402 includes data symbols from $B(1)$ to $B(n/3)$, second group of data symbols 403 includes data symbols from $B(n/3 + 1)$ to $B(2n/3)$, and third group of data symbols 404 includes data symbols from $B(2n/3 + 1)$ to $B(n)$. An interleaved block of symbols 106 is formed by an alternating selection of data symbols from first, second and third groups of data symbols 402-04 by a data symbol counter 408. The alternating selection of data symbols begins in first group of data symbols 402 beginning from the $B(1)$ data symbol, alternating to second group of data symbols 403 beginning from the $B(2n/3)$ data symbol and alternating next to third group of data symbols 404 beginning from the $B(n)$ data symbol. The alternating selection ends with the $B(n/3)$ data symbol in first group of data symbols 402, the $B(2n/3)$ data symbol in second group of data symbols 403 and the $B(2n/3 + 1)$ data symbol in third group of data symbols 404.

The alternating selection of data symbols from first group of data symbols 402 is according to a forward addressing beginning from the $B(1)$ to $B(n/3)$ data symbols; such that after the data symbol $B(1)$ is selected, the next selection from first group of data symbols 402 includes the data symbol $B(2)$. The alternating selection of data symbols from second group of data symbols 403 is according to a forward addressing beginning from the $B(n/3 + 1)$ to $B(2n/3)$ data symbols, such that after the data symbol $B(n/3 + 1)$ is selected, the next selection from second group of data symbols 403 includes the data symbol $B(2n/3 + 1)$. In an alternative embodiment, the alternating selection of data symbols from second group of data symbols 403 is according to a backward addressing beginning from the $B(2n/3)$ to $B(n/3 + 1)$ data symbols. The alternating selection of data symbols from third group of data symbols 404 is according to a backward addressing beginning from the $B(n)$ to $B(2n/3 + 1)$ data symbols, such that after the data symbol $B(n)$ is selected, the next selection from third group of data symbols 404 is the data symbol $B(n-1)$.

Various aspects of the invention further includes receiving a plurality of coded data symbols 104 in a block of coded data symbols by an interleaver 409, and interleaving block of coded data symbols 104 according to a single bit reversal interleaving function to form block of data symbols 401. The bit reversal interleaver 409 input symbols are written sequentially at

addresses, N_{in} from 0 to block length minus 1, then the symbols are read out of the interleaver from addresses $N_{out} = 2^m (N_{in} \bmod N) + \text{Bit_Rev}_m(\lfloor N_{in}/N \rfloor)$, where $\lfloor x \rfloor$ denotes the largest integer less than or equal to x , and $\text{Bit_Rev}_m(y)$ denotes the bit-reversed m -bit value of y .

- 5 Generally, the invention includes in various aspects, an interleaving method and accompanying apparatus for implementing an OTD feature of a CDMA communication system. A single bit reversal interleaver is used followed by a forward/backward addressing scheme. The interleaved symbols are grouped in a specific order. Two counters may be used to get
- 10 the addresses of the output symbols. The first counter points to the beginning of the interleaved symbols and is incremented while the second counter points to the end of the interleaved symbols and is decremented. The two symbols are then sent consecutively. In a case of MC-OTD system, the grouping of the symbols after the interleaver may include 3 counters,
- 15 the first and second counter point to the beginning and 2/3 of the interleaved symbols, respectively, and are incremented. The third counter points to the end of the interleaved symbols and is decremented. The advantages of the invention include absence of a requirement for any antenna swapping function or cyclic shifts to mitigate the effect of
- 20 correlated antenna fading while ensuring consecutive coded symbols are not punctured due to the reverse power control sub-channel.

The bit reversal interleaver input symbols are written sequentially at addresses, N_{in} from 0 to block length minus 1, then the symbols are read out of the interleaver from addresses $N_{out} = 2^m (N_{in} \bmod N) + \text{Bit_Rev}_m(\lfloor N_{in}/N \rfloor)$, where $\lfloor x \rfloor$ denotes the largest integer less than or equal to x , and

5 $\text{Bit_Rev}_m(y)$ denotes the bit-reversed m -bit value of y . The interleaver parameters are listed in Table 1 for various data rates. An example is shown in Fig. 5 for $m=7$ and $N=3$. Each number shown in the matrix of FIG. 5 represents a symbol address in an input block of data. The location where is shown in the matrix is with respect to other data symbols in an

10 output block of data symbols.

From Fig. 1 and Fig. 2 one ordinary skilled in the art may notice that the implementation of interleaver 105 from the transmitter side which may be a base station may include the following steps:

Decide if it is in OTD mode and determine the interleaver size.

15 Demultiplex the input symbols into two memory locations, even bits go to M1 and odd bits go to M2.

Assume that you can share the same bit reversal interleaver for the two segments of the data. Once the read address is generated, even bits go to M1, and odd bits go to M2.

20 Do the half block ($N/4$) shifting on M2.

Then multiplex back the two data streams into one stream.

Due to the number of frame sizes to be implemented, the addressing schemes for various memory locations on M1 and M2 need to be flexible enough to meet all frame sizes.

For the interleaving method and apparatus according to various
5 aspects of the invention, the transmitter side, for example Base Station, may perform the following steps:

Determine the interleaver size.

Perform bit reversal interleaving.

Read the interleaved data from both ends (beginning and end)

10 On the receiving side, for example Mobile Station, to de-interleave the incoming data stream, the Mobile Station needs to go through the same procedures in a reverse order. One ordinary skilled in the art may appreciate that interleaving/de-interleaving method and apparatus according to various aspects of the invention includes fewer steps than
15 prior art method and apparatus. The same applies to the MC-OTD case. As a result, the data symbol interleaving according to various aspects of the invention has the advantage of lower complexity.

What is claimed is:

Claims

1. In a communication system, a method for interleaving data symbols of a block of data symbols having data symbols from $B(1)$ to $B(n)$ comprising the steps of:

5

dividing said block of data symbols into a plurality of groups of data symbols, wherein each of said plurality of groups of data symbols including data symbols equal in number to a fraction of data symbols $B(1)$ through $B(n)$;

10 forming an interleaved block of data symbols by an alternating selection of data symbols from said plurality groups of data symbols.

2. The method as recited in claim 1 wherein said alternating selection from each of said plurality of group of data symbols is according to either a
15 forward addressing or a backward addressing mappings.

3. The method as recited in claim 1 further comprising the steps of:
receiving a plurality of coded data symbols in a block of coded data symbols;
20 interleaving said block of coded data symbols according to a single bit reversal interleaving function to form said block of data symbols.

4. In a communication system, a method for interleaving data symbols of a block of data symbols having data symbols from $B(1)$ to $B(n)$ comprising the steps of:

5 dividing said block of data symbols into a first and second groups of data symbols, wherein said first group of data symbols including data symbols from $B(1)$ to $B(n/2)$, and said second group of data symbols including data symbols from $B(n/2 + 1)$ to $B(n)$;
forming an interleaved block of data symbols by an alternating selection of
10 data symbols from said first and second groups of data symbols.

5. The method as recited in claim 4 wherein said alternating selection begins in said first group of data symbols beginning from said $B(1)$ data symbol and alternating to said second group of data symbols beginning
15 from said $B(n)$ data symbol.

6. The method as recited in claim 5 wherein said alternating selection ends with said data symbol $B(n/2)$ in said first group of data symbols and said data symbol $B(n/2 + 1)$ in said second group of data symbols.

20

7. The method as recited in claim 4 wherein said alternating selection from said first group of data symbols is according to a forward addressing beginning from said $B(1)$ to $B(n/2)$ data symbols.

5 8. The method as recited in claim 4 wherein said alternating selection from said second group of data symbols is according to a backward addressing beginning from said $B(n)$ to $B(n/2 + 1)$ data symbols.

9. The method as recited in claim 4 further comprising the steps of:
10 receiving a plurality of coded data symbols in a block of coded data symbols;
interleaving said block of coded data symbols according to a single bit reversal interleaving function to form said block of data symbols.

10. In a communication system, a method for interleaving data symbols of a block of data symbols having data symbols from $B(1)$ to $B(n)$ comprising the steps of:

dividing said block of data symbols into a first, second and third
5 groups of data symbols, wherein said first group of data symbols including data symbols from $B(1)$ to $B(n/3)$, said second group of data symbols including data symbols from $B(n/3 + 1)$ to $B(2n/3)$, and said third group of data symbols including data symbols from $B(2n/3 + 1)$ to $B(n)$;

forming an interleaved block of symbols by an alternating selection of
10 data symbols from said first, second and third groups of data symbols.

11. The method as recited in claim 10 wherein said alternating selection begins in said first group of data symbols beginning from said $B(1)$ data symbol and alternating to said second group of data symbols beginning
15 from said $B(2n/3)$ data symbol and alternating to said third group of data symbol beginning from said $B(n)$ data symbol.

12. The method as recited in claim 10 wherein said alternating selection ends with said $B(n/3)$ data symbol in said first group of data symbols, said
20 $B(2n/3)$ data symbol in said second group of data symbols and said $B(2n/3 + 1)$ data symbol in said third group of data symbols.

13. The method as recited in claim 10 further comprising the steps of:
receiving a plurality of coded data symbols;

interleaving said coded data symbols according to a single bit
5 reversal interleaving function to form said block of data symbols.

14. The method as recited in claim 10 wherein said alternating selection
from said first group of data symbols is according to a forward addressing
beginning from said $B(1)$ to $B(n/3)$ data symbols.

10

15. The method as recited in claim 10 wherein said alternating selection
from said second group of data symbols is according to a forward
addressing beginning from said $B(n/3 + 1)$ to $B(2n/3)$ data symbols.

15 16. The method as recited in claim 10 wherein said alternating selection
from said second group of data symbols is according to a backward
addressing beginning from said $B(2n/3)$ to $B(n/3 + 1)$ data symbols.

17. The method as recited in claim 10 wherein said alternating selection
20 from said third group of data symbols is according to a backward
addressing beginning from said $B(n)$ $B(2n/3 + 1)$ data symbols.

18. In a communication system, an apparatus for interleaving data symbols of a block of data symbols having data symbols from B(1) to B(n) comprising:

5 means for dividing said block of data symbols into a plurality of groups of data symbols, wherein each of said plurality of groups of data symbols including data symbols equal in number to a fraction of data symbols B(1) through B(n);

 means for forming an interleaved block of data symbols by an
10 alternating selection of data symbols from said plurality groups of data symbols.

19. The apparatus as recited in claim 18 wherein said alternating selection from each of said plurality of group of data symbols is according
15 to either a forward addressing or a backward addressing mappings.

20. The apparatus as recited in claim 18 further comprising:

 means for receiving a plurality of coded data symbols in a block of coded data symbols;

means for interleaving said block of coded data symbols according to a single bit reversal interleaving function to form said block of data symbols.

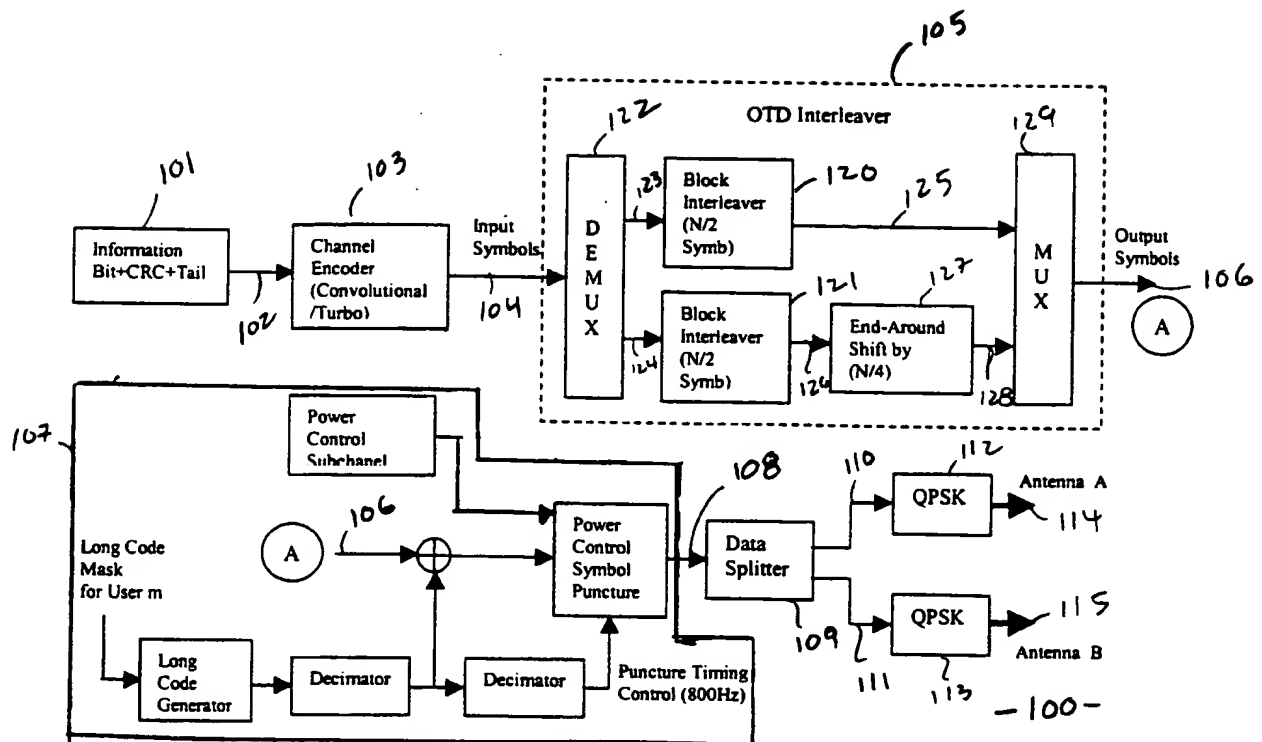


Fig. 1 Current Interleaving scheme for OTD.

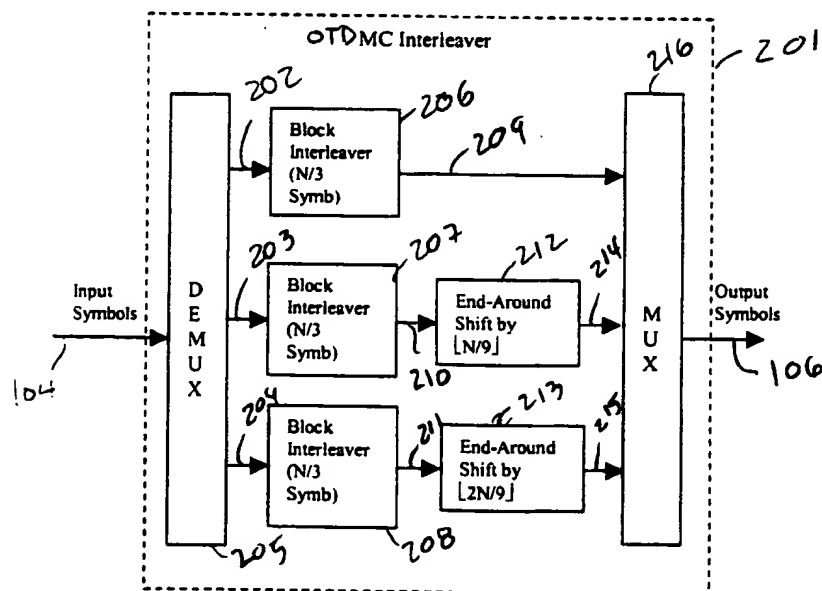


Fig. 2 Current Interleaving scheme for MC.

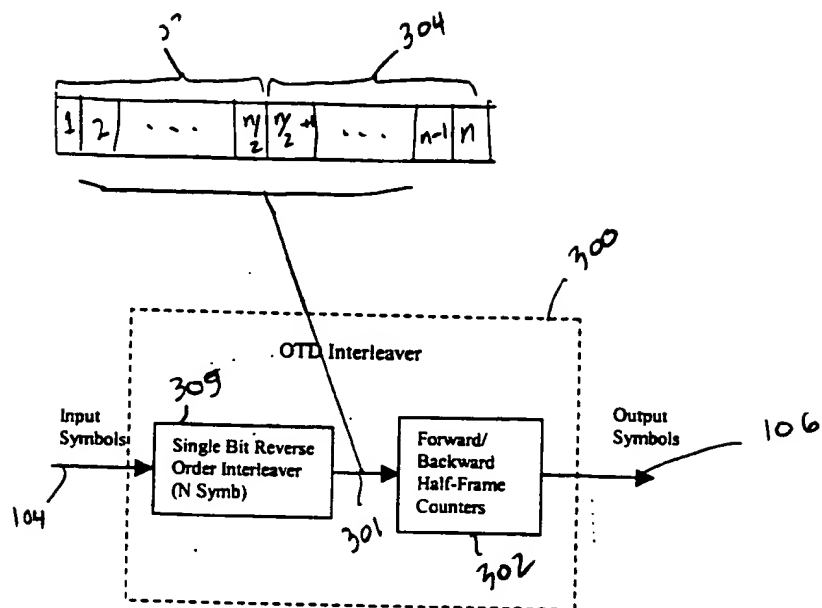


Fig. 3 Interleaving Scheme

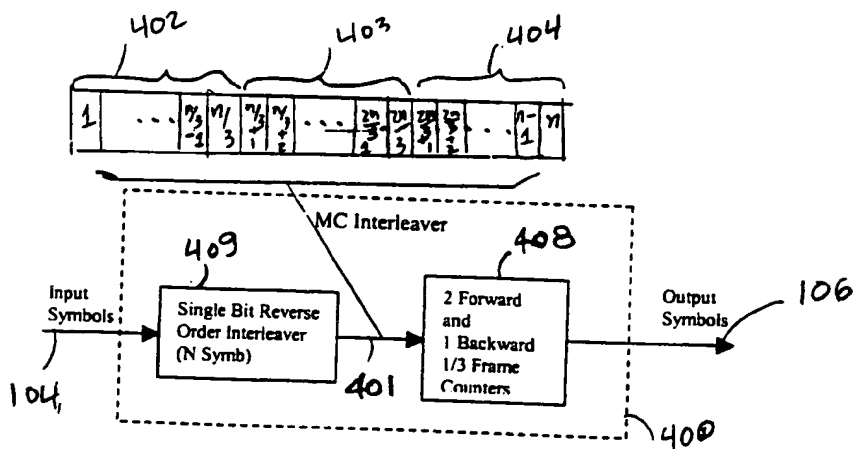


Fig. 4 Interleaving scheme for MC OTD

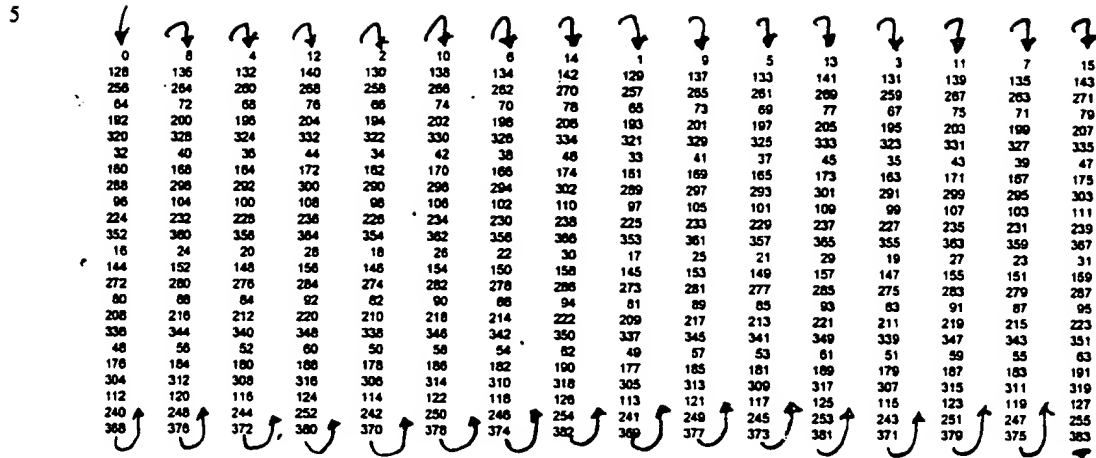


Table 1. Interleaver Parameters

Interleaver Size	m	N
48	4	3
96	5	3
192	6	3
384	7	3
768	7	6
1536	7	12
3072	7	24
6144	8	24
12288	8	48
72	3	9
144	4	9
288	5	9
576	6	9
1152	7	9
2304	7	18
4608	8	18
9216	8	36
18432	9	36
36384	9	72
73728	10	72
147456	10	144
128	7	1

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US00/03904

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) :H04L 25/49; H04J 13/00

US CL :Please See Extra Sheet.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 370/320, 334, 335, 342, 441, 479; 375/262, 265, 267, 299; 714/701, 702, 756

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X,P	ROHANI ET AL. Performance Analysis of CDMA Transmit Diversity Methods IEEE, April 1999, pages 1326-1330.	1-20
X,E	US 6,064,664 A (KIM) 16 MAY 2000, FIGS. 9-10 and 13, col. 6, line 39 to col. 8, line 21.	1-20

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
A document defining the general state of the art which is not considered to be of particular relevance	*X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
E earlier document published on or after the international filing date	*Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
L document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	*Z* document member of the same patent family
O document referring to an oral disclosure, use, exhibition or other means	
P document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search 15 JUNE 2000	Date of mailing of the international search report 06 JUL 2000
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231 Facsimile No. (703) 305-3230	Authorized officer DUONG, FRANK Telephone No. (703) 308-5428 <i>Joni Hill</i>

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US00/03904

A. CLASSIFICATION OF SUBJECT MATTER:

US CL :

370/320, 334, 335, 342, 441, 479; 375/262, 265, 267, 299; 714/701, 702, 756